

- ✎ 31: (7) stubless adj2 configur\$8
- ✎ 35: (8) stubless near5 configur\$6
- ✎ 39: (461) point-to-point adj2 configur\$6
- ✎ 310: (43665) memory adj3 (module or board)
- ✎ 314: (44) 39 and 310
- ✎ 316: (297) stub adj2 configur\$6
- ✎ 317: (21) 310 and 316
- ✎ 321: (934837) terminat\$6
- ✎ 322: (1167) 310 same 321
- ✎ 323: (1159) 365/62
- ✎ 324: (3194) 365/63
- ✎ 325: (60301) "365"/\$6
- ✎ 326: (1664) 361/728
- ✎ 327: (706) 361/729
- ✎ 329: (7) 322 and 326
- ✎ 328: (4) 322 and 327
- ✎ 330: (62) 322 and 323
- ✎ 331: (87) 322 and 324
- ✎ 332: (342) 322 and 325
- ✎ 334: (28) (FUNABA near2 8EIJ1).In.
- ✎ 335: (236) (NISHIO near2 KAY1).In.
- ✎ 336: (26) (SHIBATA near2 YOYOKO).In.

Search	Li	Becke	Burns	Chen
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DB: US-PGPUB;USPAT.EPO;JPO;DERWENT;IBM.TDB

Default operator: **OR** ▼

✓ Photo

☒ Highlight all hit terms initially

BRS form ISLA form Image Text HTML

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	U	I	P	Document ID	Inventor	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	S	C	P	Q
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6661690 B2	Moriarty; Sharon L. et al.	20031209	19	High capacity memory module	365/63	365/62		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6142830 A	Loeffler; Steffen	20001107	7	Signaling improvement using	439/620	361/788		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6590781 B2	Kollipara; Ravindranath	20030708	28	Clock routing in multiple	361/760	174/250;		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
4	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6742098 B1	Halbert; John B. et al.	20040625	18	Dual-port buffer-to-memory	714/172	365/230.05;		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
5	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6772262 B1	Park; Myun-joo et al.	20040803	17	Memory module with improved	710/301	361/736;		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
6	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6438012 B1	Osaka; Hideki et al.	20020820	22	Directional coupling memory	365/52	365/230.03		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
7	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6392897 B1	Nakase; Yasunobu et al.	20020521	31	Circuit module	361/785	228/180.1;		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
8	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6519173 B2	Funaba; Seiji et al.	20030211	61	Memory system	365/63	365/99		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
9	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6172895 B1	Brown; Dirk D. et al.	20010109	14	High capacity memory module	365/63	365/62		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>

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